

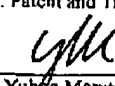
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S/N 10/518,224

JAN 04 2006

PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Torayuki Tsukada Serial: 10/518,224  
Filed: December 16, 2004 Docket No.: 10921.0261USWO  
Title: CHIP RESISTOR HAVING LOW RESISTANCE AND METHOD OF  
MAKING THE SAME

CERTIFICATE UNDER 37 CFR 1.6(d)I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on January 4, 2006.By:   
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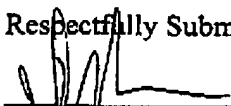
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Dear Sir:

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Respectfully Submitted,

  
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